

TABLE OF CONTENTS

1. OVERVIEW	1
2. INSTALLATION.....	2
3. SPECIFICATIONS.....	3
3.1. INPUTS.....	3
3.2. OUTPUTS.....	3
3.3. ELECTRICAL.....	3
3.4. PHYSICAL.....	3
4. STATUS LEDS.....	4
5. CIRCUIT DESCRIPTION.....	5
6. JUMPERS AND USER ADJUSTMENTS.....	6
6.1. SELECTING WHETHER LOCAL FAULTS WILL BE MONITORED BY THE GLOBAL FRAME STATUS	6
6.2. SELECTING THE INPUT AC/DC COUPLING	6
6.3. GAIN ADJUSTMENT	6
6.4. CABLE EQUALIZATION OPERATION	7

Figures

Figure 1-1: 7700ADA-ETSI Block Diagram	1
Figure 2-1: Rear Panel	2
Figure 6-1: LED and Jumper Locations.....	6

REVISION HISTORY

<u>REVISION</u>	<u>DESCRIPTION</u>	<u>DATE</u>
1.0	Original Version	Nov 2006
1.1	General format clean up. Updated Block Diagram.	Sept 2009
1.2	Updated block diagram	Jan 2013

Information contained in this manual is believed to be accurate and reliable. However, Evertz assumes no responsibility for the use thereof nor for the rights of third parties, which may be affected in any way by the use thereof. Any representations in this document concerning performance of Evertz products are for informational use only and are not warranties of future performance, either expressed or implied. The only warranty offered by Evertz in relation to this product is the Evertz standard limited warranty, stated in the sales contract or order confirmation form.

Although every attempt has been made to accurately describe the features, installation and operation of this product in this manual, no warranty is granted nor liability assumed in relation to any errors or omissions unless specifically undertaken in the Evertz sales contract or order confirmation. Information contained in this manual is periodically updated and changes will be incorporated into subsequent editions. If you encounter an error, please notify Evertz Customer Service department. Evertz reserves the right, without notice or liability, to make changes in equipment design or specifications.

1. OVERVIEW

The 7700ADA-ETSI High Amplitude Distribution Amplifier is a general purpose amplifier with an equalizing function for distributing a wide range of digital and analog signals. It has been designed to distribute pulses and signals up to $\pm 3.5\text{V}$ ITU-T G.703 E12 (Physical/electrical characteristics of hierarchical digital interface at 2048kb/s) signals. The 7700ADA-ETSI features one balanced equalized input with four outputs.

The 7700ADA-ETSI occupies one card slot and can be housed in a 1RU frame which will hold up to 3 modules, a 3RU frame which will hold up to 15 modules, a 350FR portable frame which will hold up to 7 modules or a standalone enclosure that will hold 1 module.

Features:

- 75 Ohm input impedance
- Distribute signals up to $\pm 3.5\text{V}$ to 30MHz
- High common mode range and common mode rejection ratio (CMRR)
- $\pm 5.5\text{dB}$ Gain control range
- Jumper selectable AC or DC coupling
- Cable equalizer adjustment range: 0 to 300m of Belden 8281 or 1694A
- Consistent input impedance if card power is lost

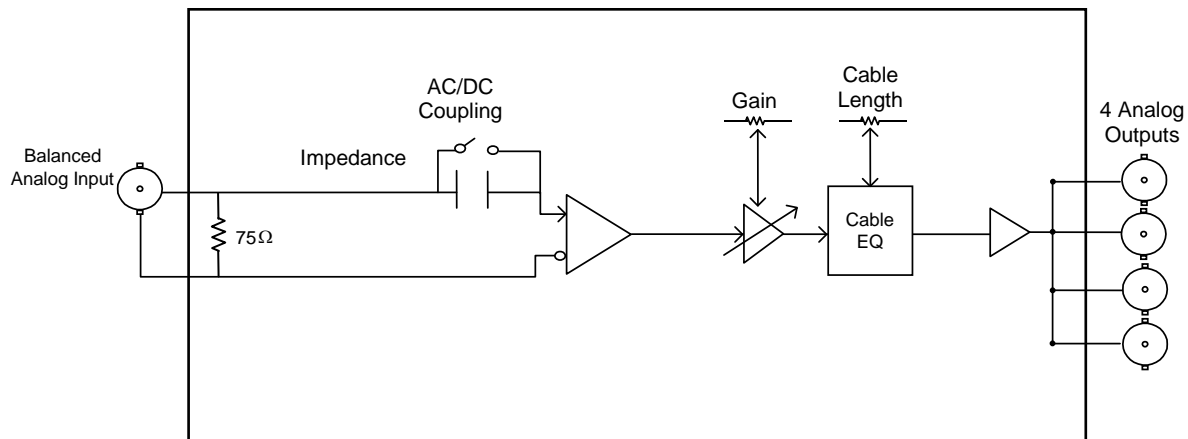


Figure 1-1: 7700ADA-ETSI Block Diagram

2. INSTALLATION

The 7700ADA-ETSI comes with a companion rear panel and occupies one slot in the 7700FR frame. Figure 2-1 shows a diagram of the rear panel. For information on inserting the module into the frame see the 7700FR manual section 3.

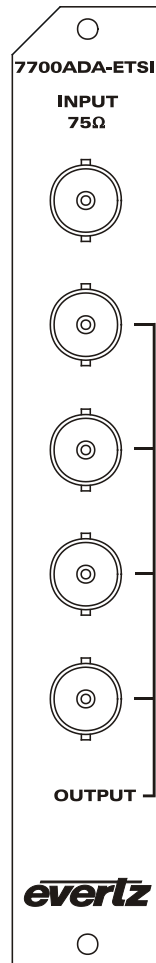


Figure 2-1: Rear Panel

INPUT: Isolated BNC connector for input signal. The input signal is terminated with 75 ohms. The INPUT jumper located on the module near the back determines whether the input signal will be AC or DC coupled. (See section 6.2)

OUT 1 to 4: There are four BNC connectors with equalized and level adjusted copies of the input signal.

3. SPECIFICATIONS

All specifications, unless otherwise indicated, are measured under the following conditions:

- 1 Vp-p video applied
- 75 Ohm source impedance signal
- DC coupled
- Gain adjusted for unity operation into 75 Ohm load
- Cable equalizer set to 0 length

3.1. INPUTS

Standards: Any analog waveform up to $\pm 3.5V$ and 30MHz bandwidth
Connector: 1 BNC per IEC 61169-8 Annex A
Common Mode Range: $>6Vp-p$
CMRR: $>60dB$ to 1 kHz
Signal Amplitude: up to $\pm 3.5V$
Cable Equalizer: 0 to 300m of Belden 8281 or 1694 cable
Impedance: 75Ohms
Coupling: AC or DC (jumper selectable)
Return Loss: $>40dB$ to 10MHz, $>25dB$ to 30MHz

3.2. OUTPUTS

Number of Outputs: 4 per card
Connector: 1 BNC per IEC 61169-8 Annex A
Output Impedance: 75 Ohm
Gain Control Range: $\pm 6dB$
Freq. Response: $< \pm 0.05dB$ no equalization (to 5.5MHz)
 $< \pm 0.09dB$ for 5 to 100m Belden 8281 or 1694A (to 5.5 MHz)
 $< \pm 0.15dB$ for 100 to 300m Belden 8281 or 1694A (to 5.5 MHz)
Differential Gain: $< 0.25\%$ 0 to 300m (for single load)
Differential Phase: < 0.19 deg 0 to 300m
C/L Gain Inequality: $< +/- 0.1\%$ for all cable lengths
C/L Delay: $< +/- 2nsec$
Output Isolation: $> 42dB$ to 10MHz, $> 32 dB$ to 30MHz
Output Return Loss: $> 40dB$ to 30MHz
Noise Performance: $< -68dB$ RMS NTC7 weighting,
 $< -60dB$ RMS 15 kHz to 5.5MHz

3.3. ELECTRICAL

Voltage: + 12VDC
Power: 2.5 Watts

3.4. PHYSICAL

Number of Slots: 1

4. STATUS LEDES

Two large LEDs on the front of the board indicate the general health of the module.

LOCAL FAULT: This Red LED indicates poor module health and will be illuminated when a local input power fault exists (i.e.: a blown fuse). The LOCAL FAULT indication can also be reported to the frame through the FRAME STATUS jumper.

MODULE OK: This Green LED indicates good module health. It will be On when the board power is good.

5. CIRCUIT DESCRIPTION

The input signal enters the board through a BNC with isolated ground so that balanced input processing may be done to remove any common mode hum that may have been added to the signal and ground shield. The input coupling (AC/DC) can be selected using an on-board jumper (J4).

An adjustable gain stage feeds the cable equalizer that can adjust the frequency response to match the attenuation of up to 300m of coaxial cable. One OP-Amp drives the four output BNC's with 75-Ohm output impedance.

6. JUMPERS AND USER ADJUSTMENTS

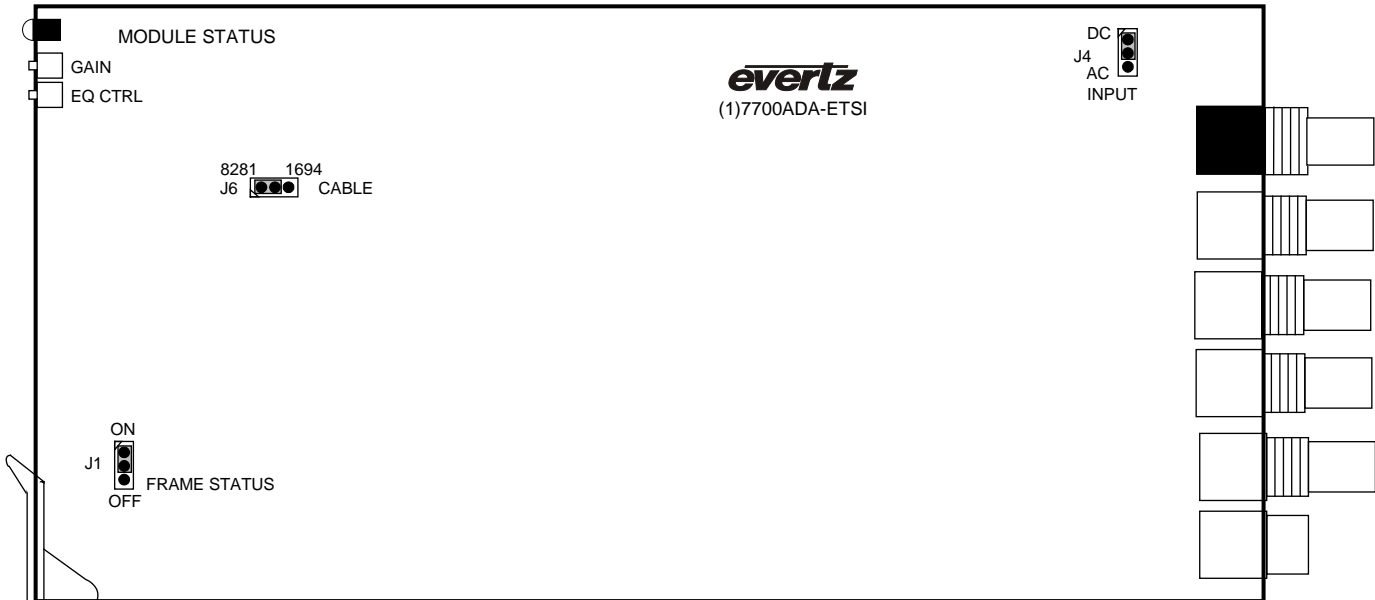


Figure 6-1: LED and Jumper Locations

6.1. SELECTING WHETHER LOCAL FAULTS WILL BE MONITORED BY THE GLOBAL FRAME STATUS

The FRAME STATUS jumper J1, located at the front of the module, determines whether local faults (as shown by the Local Fault indicator) will be connected to the 7700FR frame's global status bus.

FRAME STATUS: To monitor faults on this module with the frame status indicators (on the power supply's FRAME STATUS LEDs and on the Frame's Fault Tally output) install this jumper in the On position.

When this jumper is installed in the Off position local faults on this module will not be monitored.

6.2. SELECTING THE INPUT AC/DC COUPLING

The input may be AC or DC (default) coupled into the input-circuitry using jumper J4. In some non-video applications that do not have DC information (i.e. digital AES audio), AC coupling can be used to remove any DC level that may have built up in its transmission.

6.3. GAIN ADJUSTMENT

The **GAIN** POT on the cards front edge allows you to adjust the input signal level. Turning the POT clockwise will increase the gain.

6.4. CABLE EQUALIZATION OPERATION

With the cable equalizer **EQCTRL** POT set fully counter-clockwise, the cable equalizer will be set to the "zero length". As you turn the POT clockwise, more cable equalization is added. To set the POT to the correct position, perform the following calibration:

1. Supply the cable being equalized with a frequency response test signal.
2. Make sure the driving level from the test generator is calibrated to unity.
3. Set the cable type jumper (J6) to the type of cable you are using (either 8281 or 1694). If you are using a different type, then use either setting.
4. Make sure the test gear is terminated properly.
5. Calibrate the GAIN level POT for unity output at low frequencies.
6. Adjust the "EQCTRL" POT until the 7700ADA-ETSI output signal has flat frequency response.

This page left intentionally blank